

AUTOMATIC GAIN CONTROLLING AMPLIFIER DEVICE

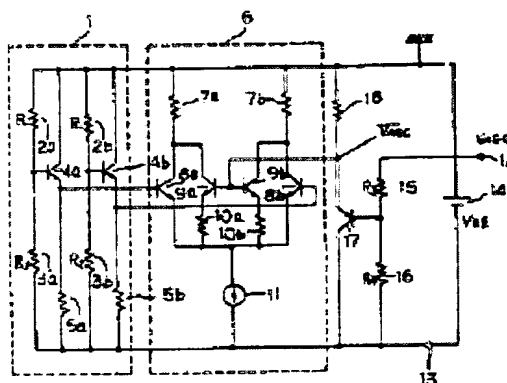
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Abstract of JP61198805

PURPOSE: To relieve the load of operation of an AGC feedback system by constituting the titled amplifier with a resistor dividing a gain setting reference voltage depending on fluctuation of a power voltage and a level shift circuit so as to suppress the gain fluctuation of an automatic gain control amplifier due to fluctuation of the power voltage.

CONSTITUTION: Voltage dividing resistor 15, 16 are two voltage dividing resistors R3, R4 arranged between a gain setting reference voltage source and a power supply and the element value is selected so as to make it equal to a resistance ratio of the input bias resistance. The gain fluctuation of an automatic gain control amplifier is suppressed to zero due to power voltage fluctuation by matching the input DC bias fluctuation of a variable gain amplifier 6 and the gain fluctuation of the automatic gain control amplifier due to power voltage fluctuation and the condition is expressed in equation. In satisfying the condition, the gain fluctuation due to the power voltage fluctuation of the automatic gain control amplifier is suppressed by applying the voltage of a connecting point between the two voltage dividing resistors R315 and R416 to the gain setting reference voltage supply terminal.

$$\frac{R_1}{R_1 + R_2} = \frac{R_3}{R_3 + R_4}$$



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